

FIG. 1
(PRIOR ART)

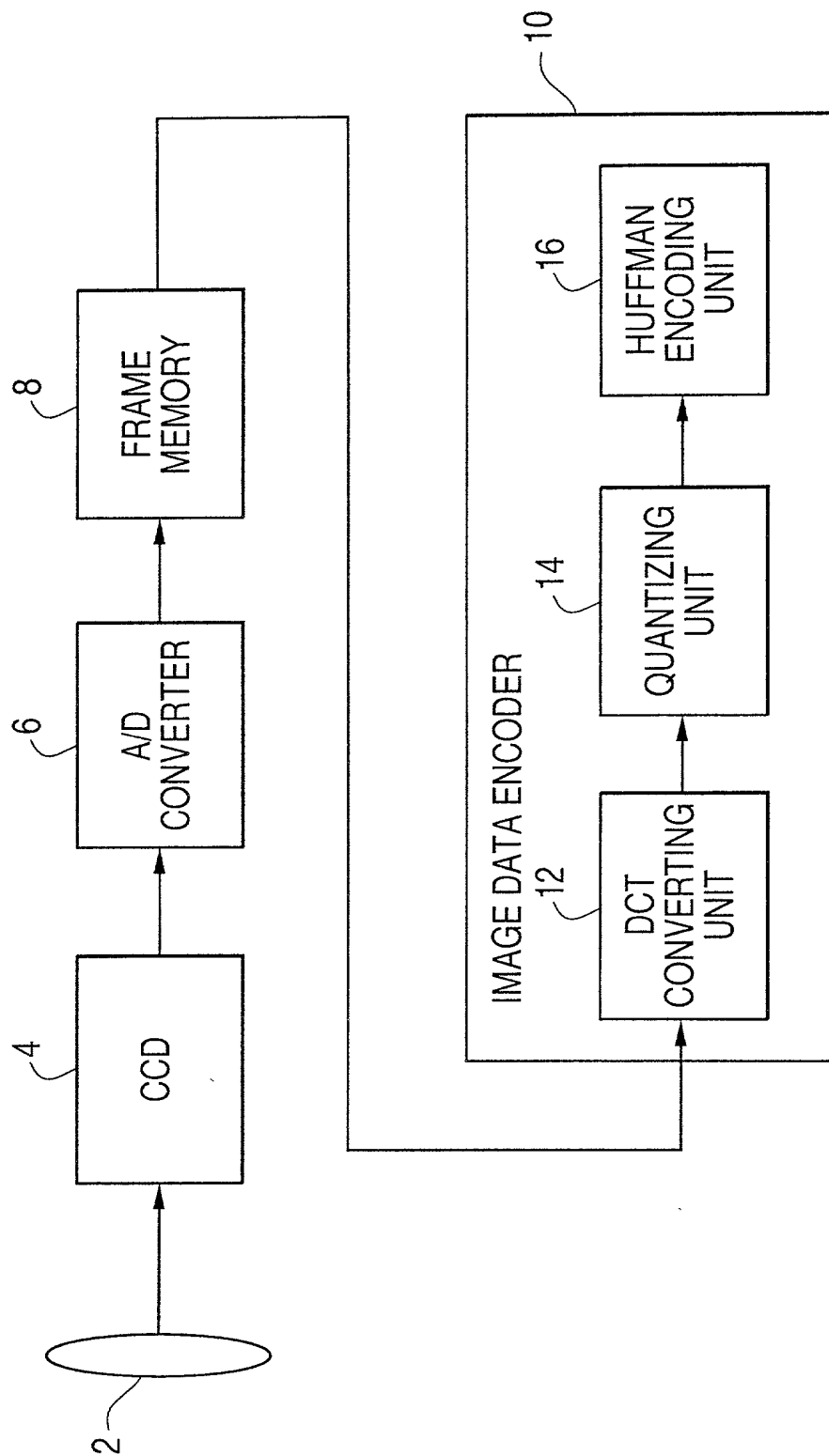


FIG. 2
(PRIOR ART)

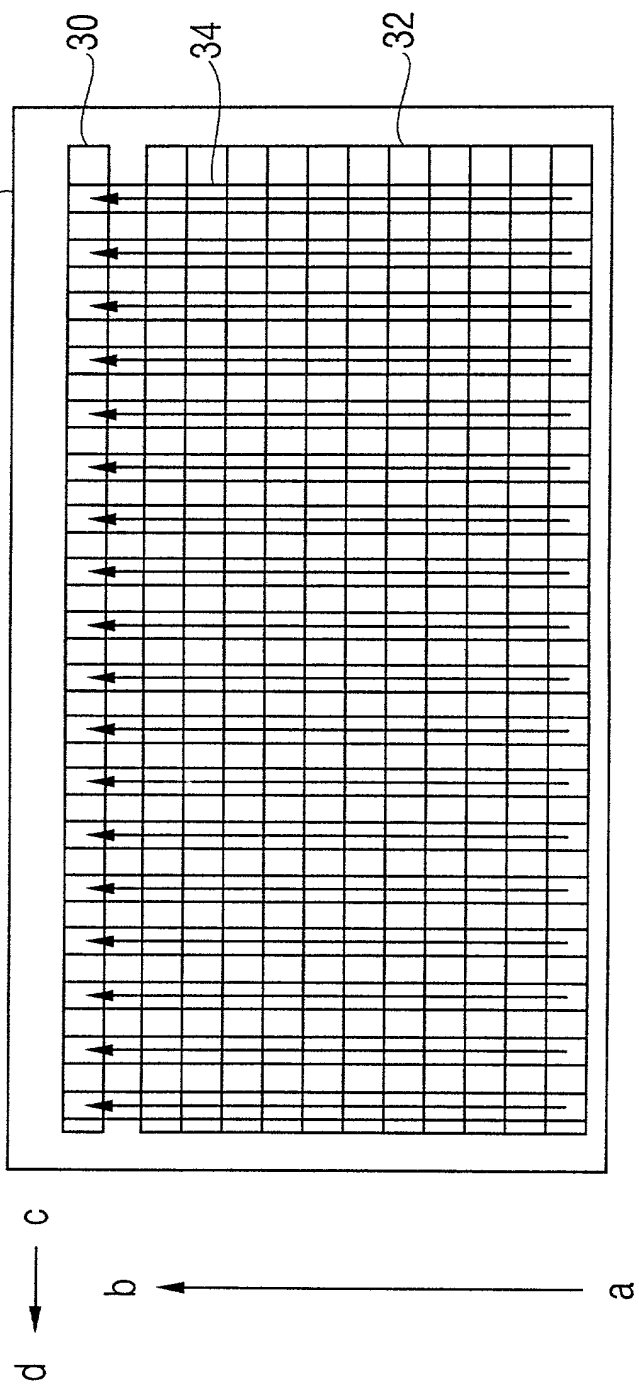


FIG. 3A
(PRIOR ART)

0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55
56	57	58	59	60	61	62	63

FIG. 3B
(PRIOR ART)

0	1	2	3	4	5	6	7
8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31
32	33	34	35	36	37	38	39
40	41	42	43	44	45	46	47
48	49	50	51	52	53	54	55
56	57	58	59	60	61	62	63

FIG. 4
(PRIOR ART)

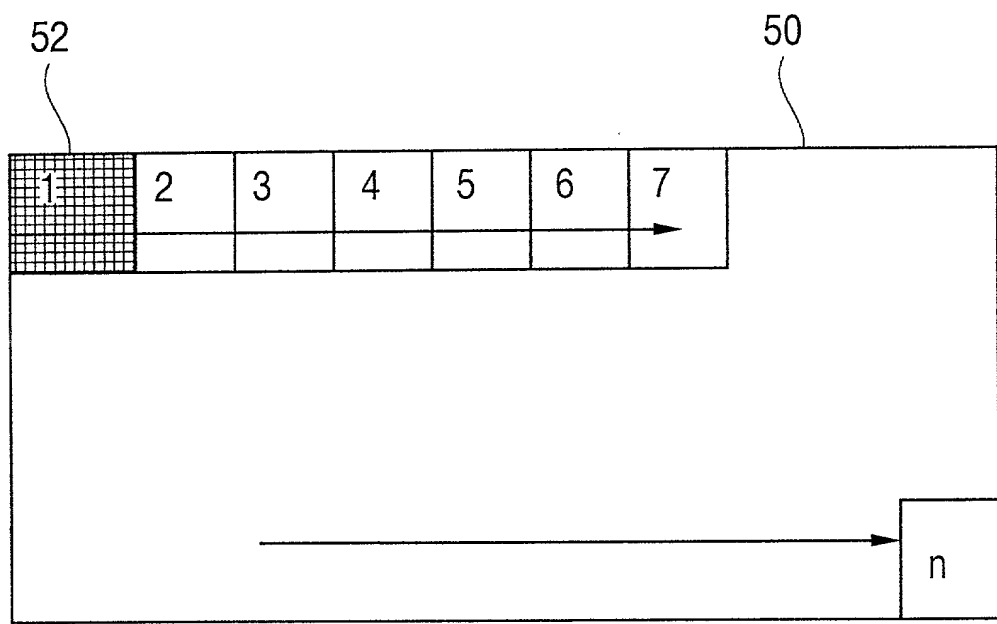
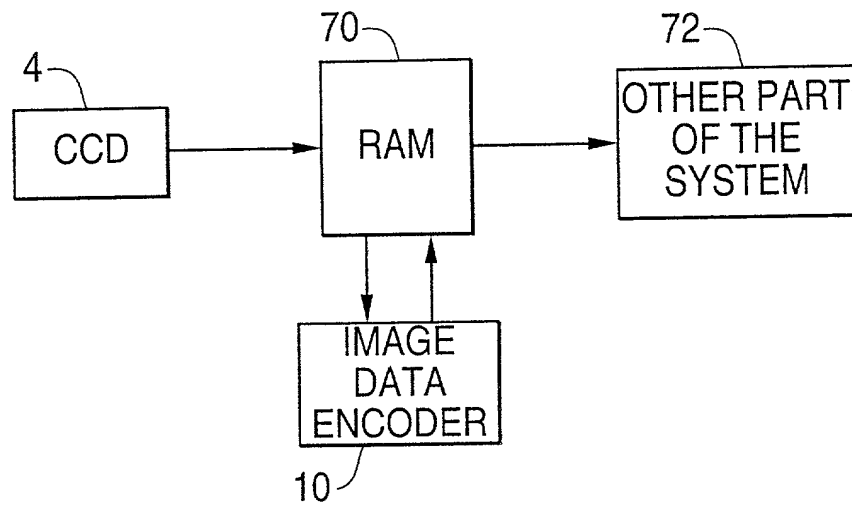


FIG. 5
(PRIOR ART)



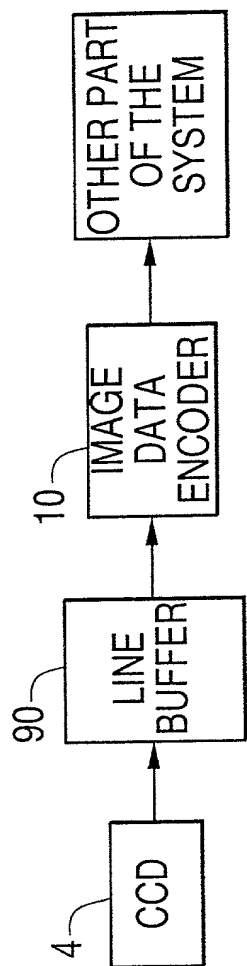


FIG. 6A
(PRIOR ART)

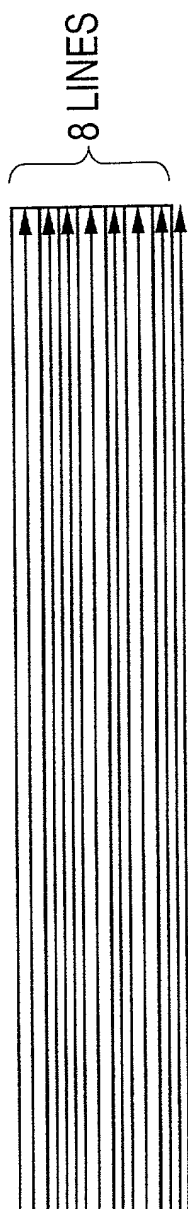


FIG. 6B
(PRIOR ART)

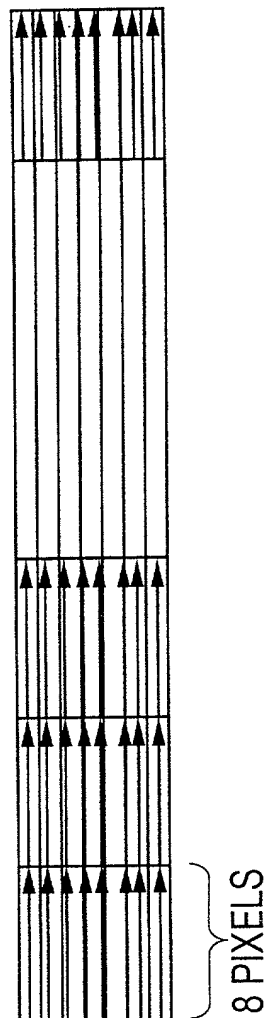


FIG. 6C
(PRIOR ART)

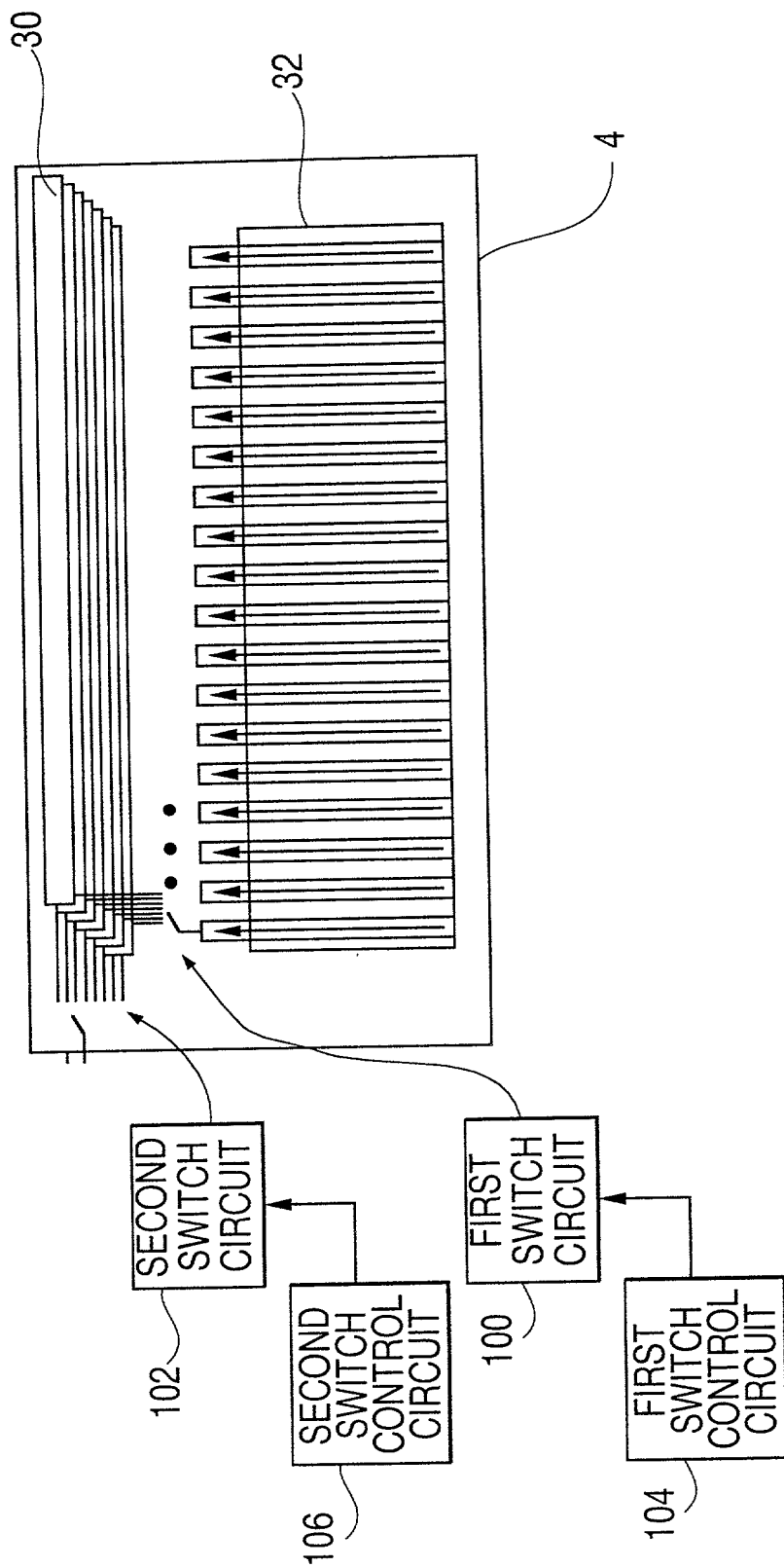


FIG. 7

FIG. 8A

1	2	3	4	5	6	7	8	9	10
11	12	13	14	15	16	17	18	19	20
21	22	23	24	25	26	27	28	29	30
31	32	33	34	35	36	37	38	39	40
41	42	43	44	45	46	47	48	49	50
51	52	53	54	55	56	57	58	59	60
61	62	63	64	65	66	67	68	69	70
71	72	73	74	75	76	77	78	79	80
81	82	83	84	85	86	87	88	89	90
91	92	93	94	95	96	97	98	99	100

32

FIG. 8B

BLOCK1BLOCK2BLOCK3BLOCK4BLOCK5									
1	2	3	4	5	6	7	8	9	10
11	12	13	14	15	16	17	18	19	20
21	22	23	24	25	26	27	28	29	30

120

122

124

FIG. 9

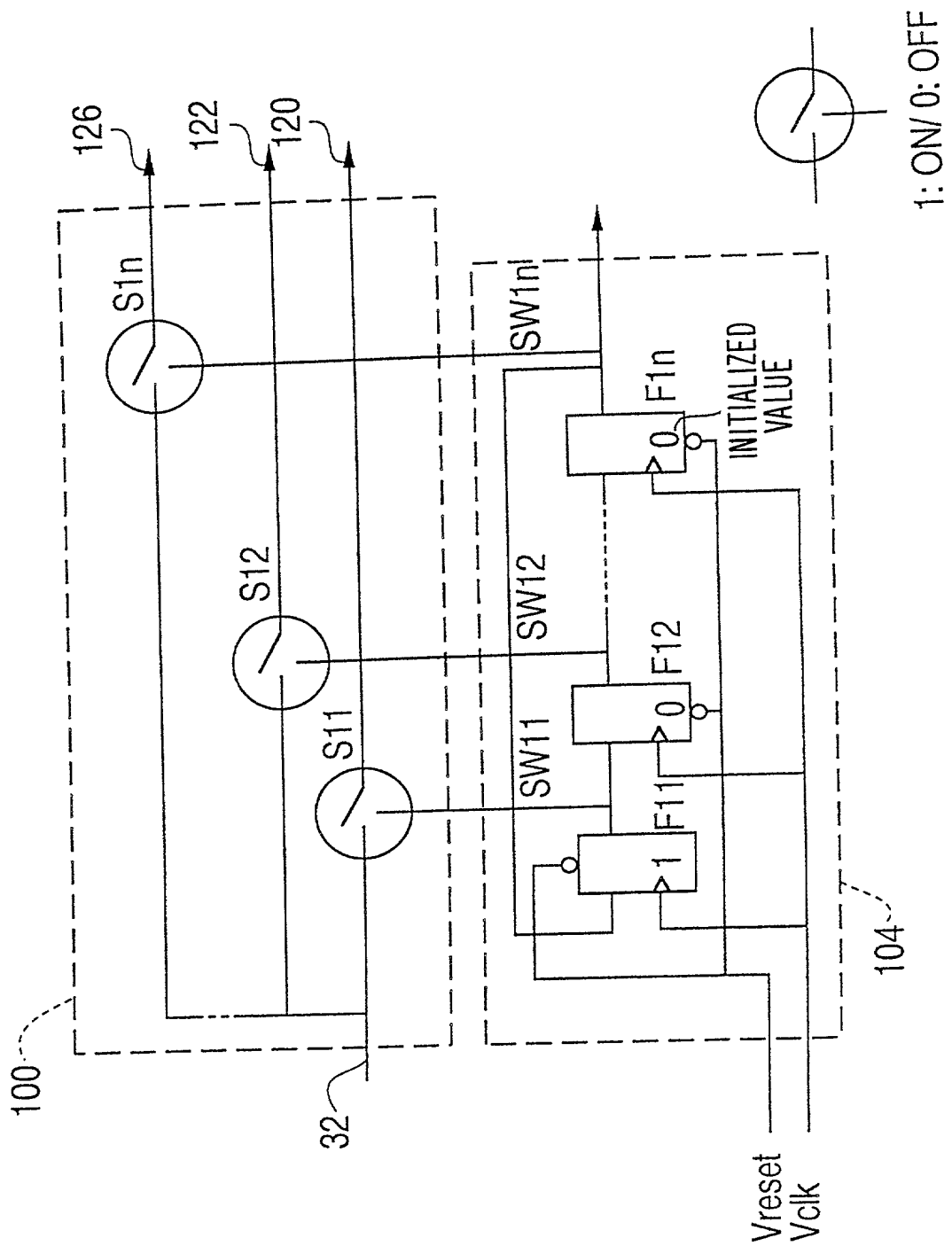


FIG. 10

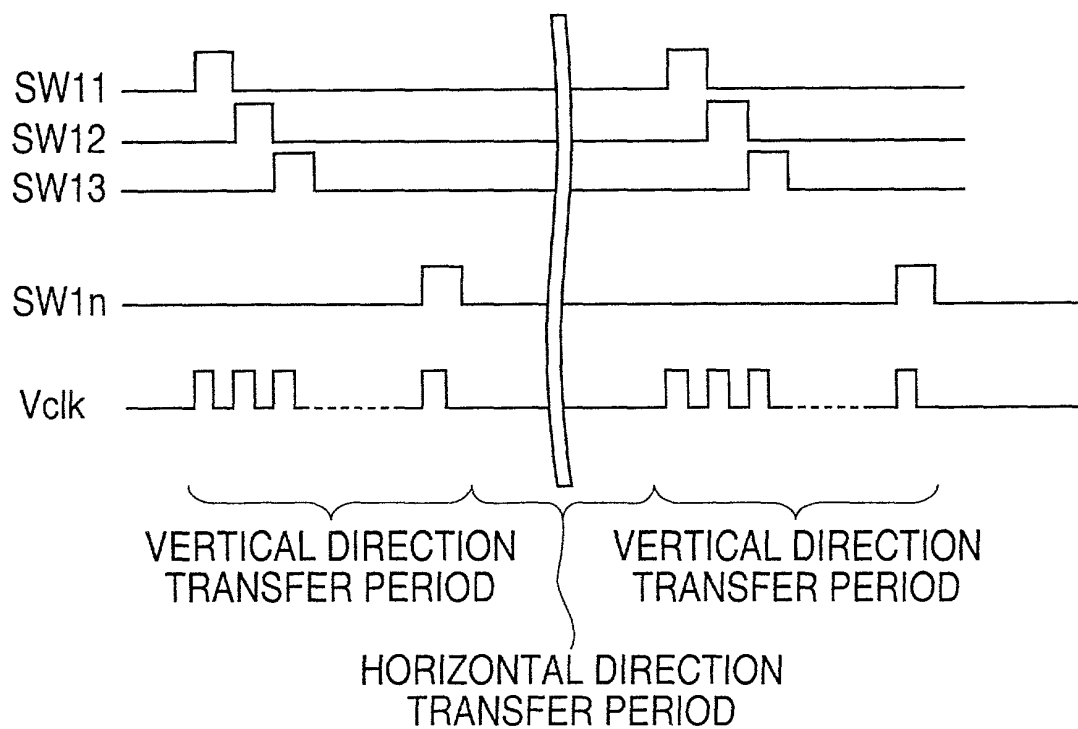


FIG. 11

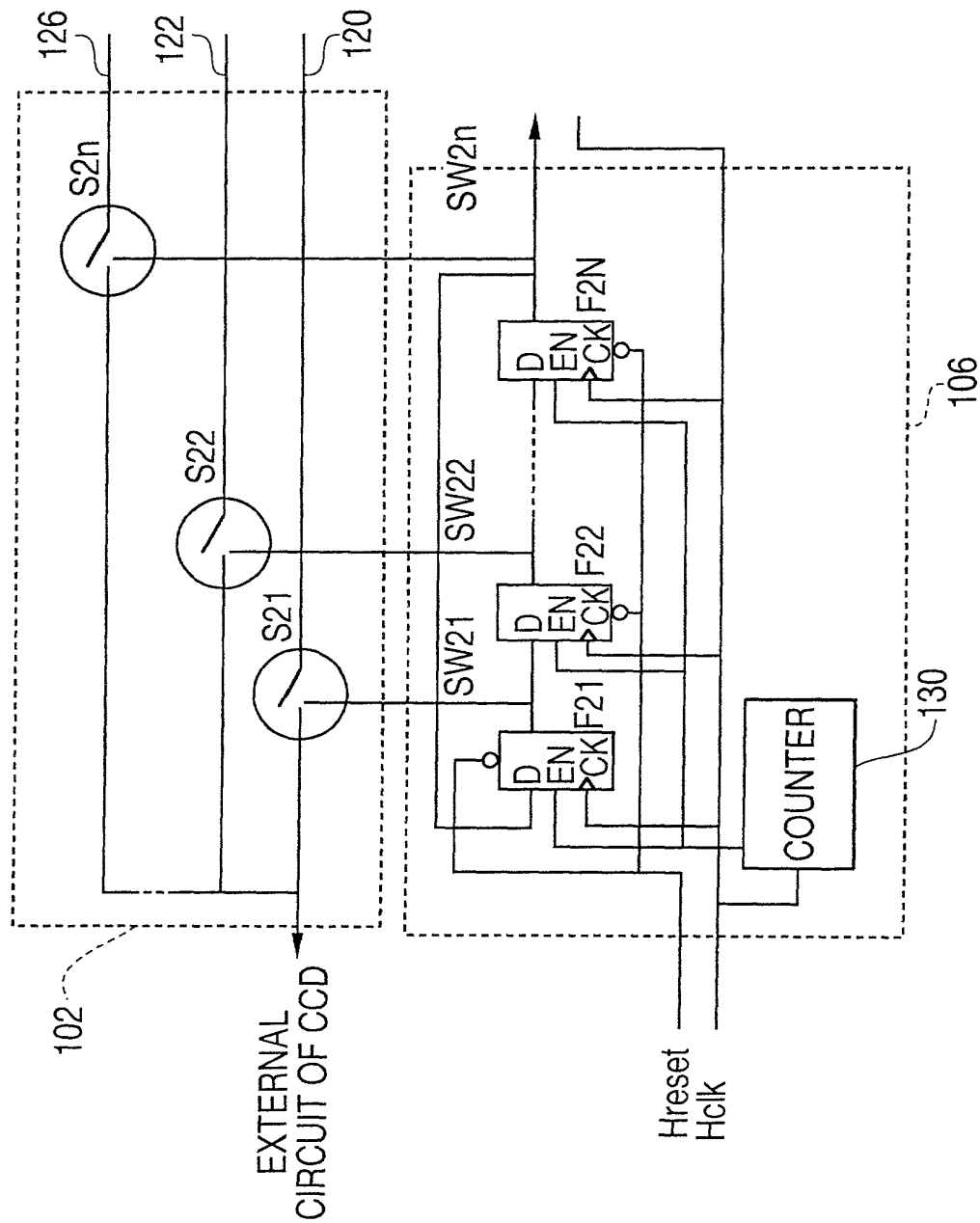
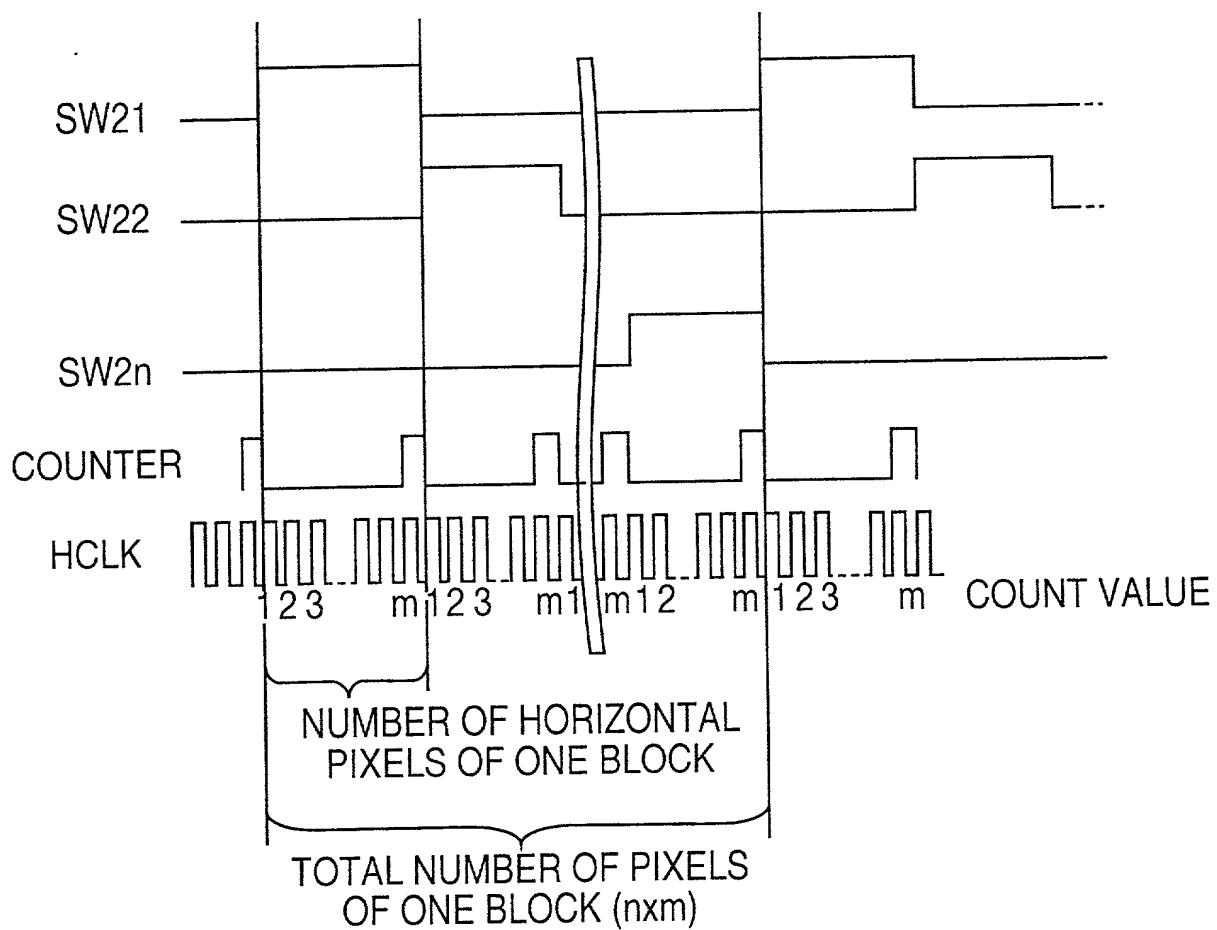
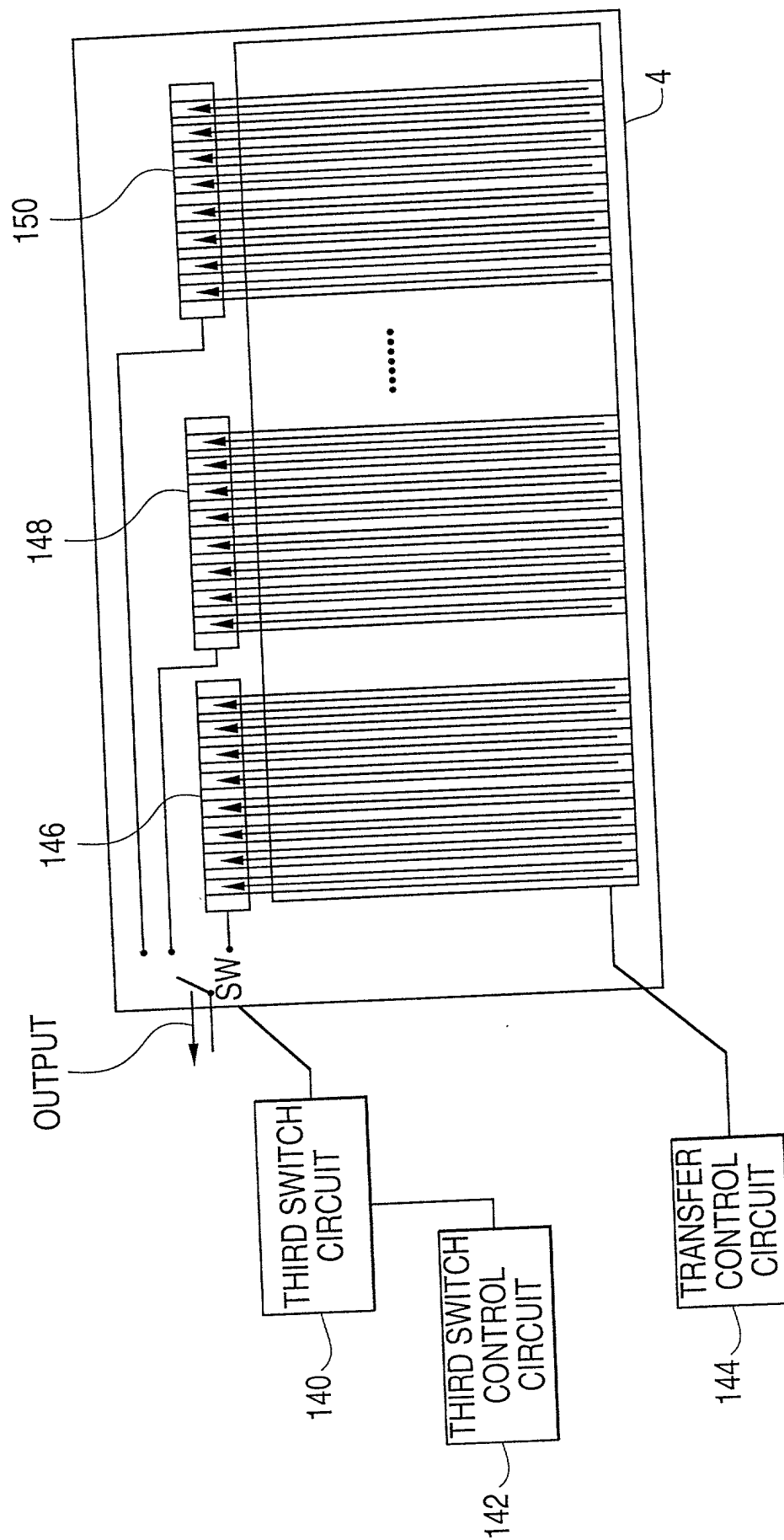


FIG. 12



The diagram illustrates a control system for a multi-channel device, labeled 4. The device 4 contains three parallel processing channels, each consisting of a first transfer control circuit (146), a second transfer control circuit (148), and a third transfer control circuit (150). These channels are connected to a common output line (OUTPUT) through a switch (SW). The switch is controlled by a third switch control circuit (140), which is connected to a third switch circuit (142). A transfer control circuit (144) is also connected to the device 4, likely providing control signals to the first and second transfer control circuits (146 and 148).



-TRANSFER PULSE TO THE K-TH BUFFER
-TRANSFER PULSE TO THE SECOND BUFFER
-TRANSFER PULSE TO THE FIRST BUFFER

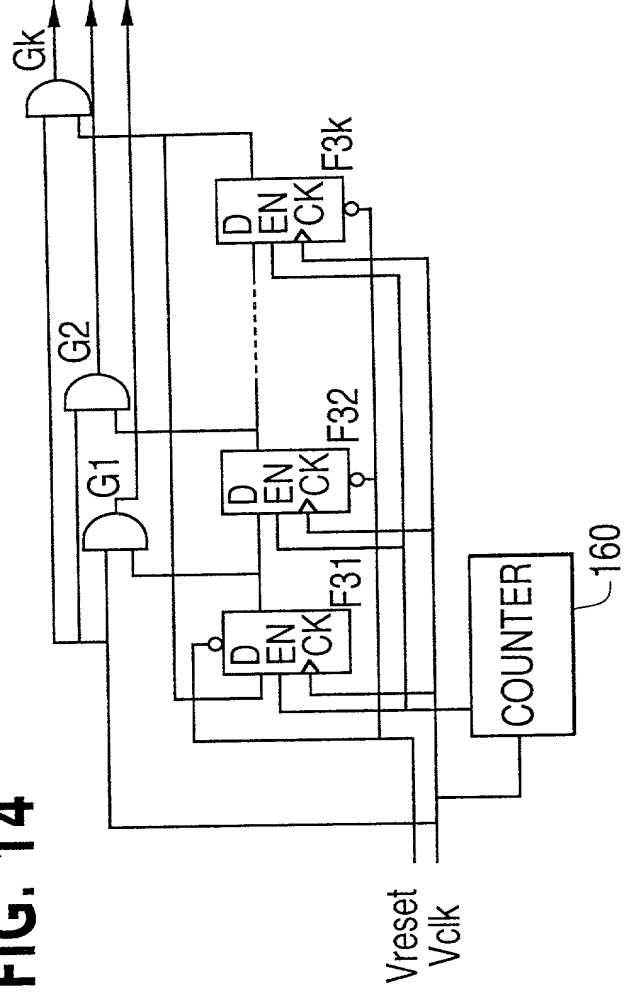


FIG. 15

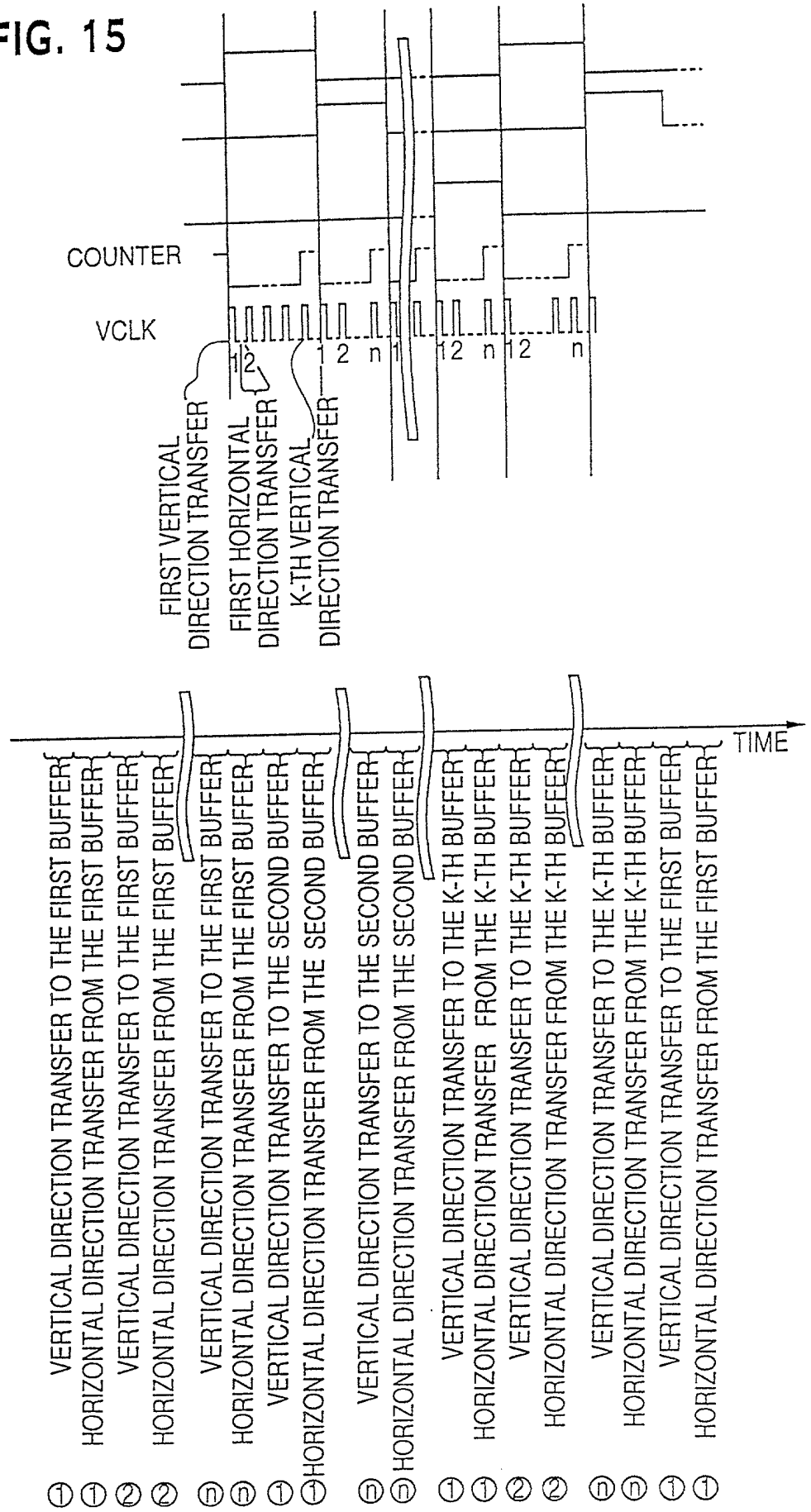


FIG. 16

